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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	BERND-ULRICH H. KLEPSER, et al., "A 10G-HZ SiGe BiCMOS Phase-Locked-Loop Frequency Synthesizer", IEEE Journal on Solid Sstate Circuits, Vol. 37, No. 3, March 2002, pp. 328-335.	
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	AC	HAMID R. RATEGH, HIRAD SAMAVATI, THOMAS H. LEE, "A 5GHz, 32mW CMOS Frequency Synthesizer With An Injection-Locked Frequency Divider", IEEE Journal on Solid State Circuits, Vol. 35, No. 5, May 2000, pp. 113-116.	
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